

Demonstrating PolKA routing approach to support traffic engineering for data-intensive science

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Motivation

- **Scientific Applications:**

- High-speed WAN networks & Multiple domains
- Massive data transfer & Large number of flows
- E2E reliability

- **Table-based forwarding bottlenecks:**

- Large number of states
- Set of shortest paths
- Latency for path configuration

- **Alternative: Source Routing (SR)**

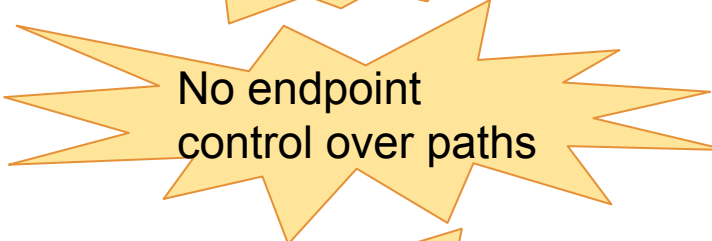
- A source specifies a path and adds a route label to the packet header.



Subutilization



Ossification



No endpoint
control over paths



Bad Congestion
Detection/Avoidance

Major Research and Development Challenges

- A Source Routing approach that meets the requirements:

open source/
interoperable

no tables in
the core

support in
prog. switches

fixed length
header

topology agnostic
multipath routing

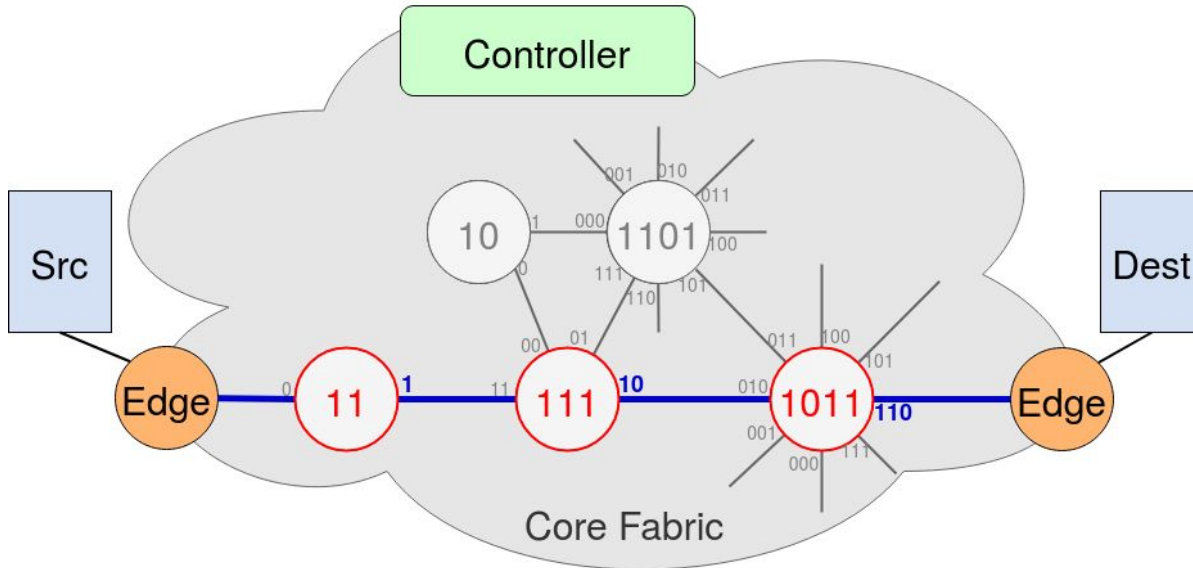
- PolKA: Polynomial Key-based Architecture for Source Routing
 - Polynomial Residue Number System (**RNS**)
 - Chinese Remainder Theorem (**CRT**)
 - Packet forwarding based on mod operation: **remainder of division**

$$\text{portID} = \langle \text{routeID} \rangle_{\text{nodeID}}$$

- mod computation reuses CRC operation (custom in Tofino switch)

How does PolKA work?

- The **Controller** chooses a **path** for a specific flow:
 - A set of switches: **nodeIDs**: {0011, 0111, 1011}
 - and their output ports: **portIDs**: {1, 10, 110}



nodeID polynomials

$$s_1(t) = t + 1 = 11$$

$$s_2(t) = t^2 + t + 1 = 111$$

$$s_3(t) = t^3 + t + 1 = 1011$$

portID polynomials

$$o_1(t) = 1$$

$$o_2(t) = t = 10$$

$$o_3(t) = t^2 + t = 110$$

Calculate routeID with CRT

$$t^4 \equiv 1 \pmod{(t + 1)}$$

$$t^4 \equiv t \pmod{(t^2 + t + 1)}$$

$$t^4 \equiv (t^2 + t) \pmod{(t^3 + t + 1)}$$

$$t^4 = 10000$$

$$\text{portID} = \langle \text{routeID} \rangle_{\text{nodeID}}$$

$$1 = \langle 10000 \rangle_{0011}$$

$$10 = \langle 10000 \rangle_{0111}$$

$$110 = \langle 10000 \rangle_{1011}$$

Innovations to be demonstrated

- Data plane
 - Source Routing with Stateless Core
 - Forwarding at line rate by **reusing CRC in P4** programmable switches
- Control plane
 - Easy to configure tunnels
 - **Integrated** in the Freerouter platform
- Potential to support:
 - Transfer of big data streams with aggregation of multiple flows
 - Big pipes/tunnels dynamically configured in the underlay network

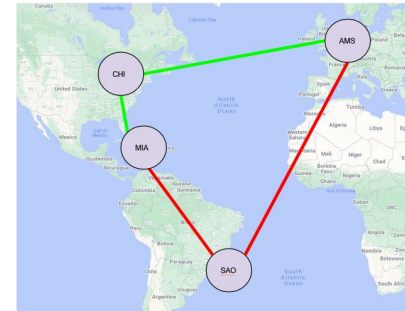
Come to see our demonstrations !!

- **Big data streams at 100 Gbps**

- PolKA@ Caltech P4 lab testbed
- Multiple aggregated TCP flows steered to pre-configured tunnels
 - A route label represents paths in the underlay network
- Comparison with Segment Routing: Both achieving line rate (100 Gbps)

- **Dynamic Traffic Steering at Intercontinental Testbed**

- PolKA@ Global P4 lab testbed
- Tunnel Setup for Traffic Engineering
 - Stateless Core
 - Define a explicit path at the Edge (route label)
 - Migration to another tunnel requires a single update at the Edge



Thank you for attention !

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